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Applicant(s) : Dureseti CHIDAMBARRAO, *et al.* Conf. No: 9755
Group Art Unit: 2812
Appln. No. : 10/667,601
Examiner: KENNEDY, Jennifer M.
Filed : September 23, 2003
For : **IMPROVED NFETs USING GATE INDUCED STRESS MODULATION**

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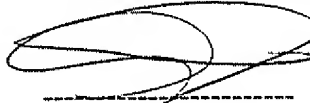
Sir:

Please direct future correspondence for the above-identified application to the following address:

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Should there be any questions or comments, the Examiner is invited to contact the undersigned at the below-listed telephone number.

Respectfully submitted,
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